Minutes #24

11/29/18

11:00 PM - 2:00PM

**Attendance**

Sobur Carim Sanni, Radhanath Purakait, Eric Luu, Shalih Miah

Recap from the 11/23/18 was done for members that missed the meeting. Questions immediately were raised about the validation of using simulation as to making the actual device. The team leader walked the group through the timeline of the class and,how unrealistic it would be to start buying components and building each design.

The meeting officially stated after 15 minutes of discussion. The feedback from mentors and sponsor were put on the table to guid each members on today's goal. The goal was to come up with cost analysis for all the designs we were considering. Also to check using a rank method the best approach for our project.

Before doing that we quickly discussed some of the flaws of the top 2 circuit we came up with and possible ways to tackle it. The first was the inthinkanator a DIY tDCS device not officially sold in market . The device is commonly used in the DIY tDCS community and the results are very promising. One thing we might be changing in the design is the potentiometer component. The final design may only have one resistor to produce a steady current for one montage.

The second Design is also one used in the open source community, except it uses a voltage regulator instead of a transistor like the first design to manage the current, we found that this circuit, while simple does not supply a reasonable current. Though the current is in fact constant.

The next meeting/group work was set for Dec 1st to work on both final report and evaluation ranks.